

# CG2400 SUP 16.00 (Software Update Package)

PCN no.: 2427

Revision	Brief Description of Changes	Date of Issue
1.0	Initial issue	2024-Nov-06 <sup>th</sup>

## 1/ Description of the change

SUP16.00 includes firmware modifications, fix of multiple issues and added improvements.

Please refer to the following table for the list of updated components :

Product	Component	SUP15.00	SUP16.00
CG2400	BIOS	V1.53.09858B31	V1.53.09858B31
	ME	04.14.804	04.14.804
	BMC	2.15.098604C7	2.16.0989838F
	FPGA	1.00	1.00
	LAN EEPROM	1.1	1.1
	KSNMP	1.4	1.4

## 2/Change Classification

<b>Classification</b>	<ul style="list-style-type: none"> <li>Maintenance release, Product improvement</li> </ul>
<b>Applicability</b>	<ul style="list-style-type: none"> <li>This SUP is field-upgradable if on SUP01.00 or higher*; please download the bundle from the FIRMWARE section of the CG2400 page of Kontron's corporate website : <a href="#">Here</a></li> </ul>

\*If you are on the Beta Firmware Release, an RMA will be required to upgrade the firmware since it is not field-upgradable.

## 3/Impact on customer's application and recommended actions

For all CG2400 units deployed with older FWs, it is recommended to apply this SUP as soon as is practical.

Please refer to the firmware upgrade procedure found below for a step-by-step upgrade process.

## 4/Firmware upgrade procedure

The following procedures will upgrade the BMC/FPGA/BIOS & 10GbE LAN

### Prerequisite

A community version of ipmitool is installed on a remote computer to enable remote monitoring—it is recommended to use ipmitool version 1.8.18.

### Updated Installation Method

Please note that when there is a ME Firmware update in the release, when sending the command in the below instructions, an additional setting will be required as follow:

#### BMC

To do the BMC update, the setting `-N 5` must be added to the command:

- i.e. `ipmitool -N 5 -I lanplus -H <BMC_IP_Addr> -U admin -P admin -z 7000 hpm upgrade <hpm_filename> force activate all`

#### BIOS

To do the BIOS update, the setting `/me` must be added to the command:

i.e. `afu1nx_64 <hpm_filename> /p /b /me` (to keep the settings)

or

`afu1nx_64 <hpm_filename> /p /b /me /n` (to reinitialize the settings)

## Upgrading the BMC and the FPGA using ipmitool

Step_1	<p>Powering off the system before performing the upgrade</p> <p>RemoteComputer_OSPrompt:~# ipmitool -I lanplus -H [BMC MNGMT_IP] -U [IPMI_USER_NAME] -P [IPMI_PASSWORD] chassis power off</p> <p><b>NOTE:</b> The upgrade can be done without a power off and the power status verification; however, when an all activate command is executed, a complete system reboot will occur.</p>
Step_2	<p>Confirm the server power status is off.</p> <p>RemoteComputer_OSPrompt:~# ipmitool -I lanplus -H [BMC MNGMT_IP] -U [IPMI_USER_NAME] -P [IPMI_PASSWORD] chassis power status</p>
Step_3	<p>Verify that the upgrade version is adequate.</p> <p>RemoteComputer_OSPrompt:~# ipmitool -z 7000 -I lanplus -H [BMC MNGMT_IP] -U [IPMI_USER_NAME] -P [IPMI_PASSWORD] hpm check</p>
Step_4	<p>To ensure you have a functional 2nd bank, you should upgrade both banks via HPM update. To do so, you need to set the FW upload selector before the update:</p> <ul style="list-style-type: none"> <li>Set both bank FW upload selector: <code>ipmitool -I lanplus -H &lt;BMC_IP_Addr&gt; -U admin -P admin raw 0x32 0x8f 0x03 0x03</code></li> <li>Verify that the answer is 0x03 (both banks) via this command: <code>ipmitool -I lanplus -H &lt;BMC_IP_Addr&gt; -U admin -P admin raw 0x32 0x8f 0x04</code></li> </ul> <p><b>Make sure to perform step_8 AFTER the upgrade to put back the selector to bank 1 for future updates.</b></p>
Step_5	<p>When upgrading from SUP03 to SUP04 or later, disabling IPMI preserve setting is necessary (to fix: BMC losing Static address after reboot)</p> <ul style="list-style-type: none"> <li>Before upgrade: <code>ipmitool -I lanplus -H &lt;BMC_IP_Addr&gt; -U admin -P admin raw 0x32 0x83 0x04 0x00</code></li> <li>After upgrade*: <code>ipmitool -I lanplus -H &lt;BMC_IP_Addr&gt; -U admin -P admin raw 0x32 0x83 0x04 0x01</code></li> </ul> <p><b>*Step_9</b></p>
<p><b>NOTE:</b> It is mandatory to wait around 5 MINUTES after BMC has BOOTED (after a power up/reboot or previous HPM upgrade) BEFORE launching a HPM upgrade. This ensures that BMC is fully booted (no unfinished setup process pending) and ready for a new HPM upgrade command.</p>	

Step_6	<p>Proceed with firmware upgrade.</p> <p>RemoteComputer_OSPrompt:~# ipmitool -N 5 -z 7000 -I lanplus -H [BMC MNGMT_IP] -U [IPMI_USER_NAME] -P [IPMI_PASSWORD] hpm upgrade &lt;HPM bundle(HPM file)&gt; all activate</p> <p><i>*If the BMC IP address is set as "Static", it will need to be reconfigured after this step to re-gain access to the BMC.(Not required if set as DHCP or upgrading from SUP04 or later)</i></p> <p><b>NOTE:</b> Wait for the upgrade to finish before performing any action on the platform. If the upgrade is interrupted, it might corrupt the data.</p>
Step_7	<p>Verify that the different components upgraded properly.</p> <p>RemoteComputer_OSPrompt:~# ipmitool -z 7000 -I lanplus -H [BMC MNGMT_IP] -U [IPMI_USER_NAME] -P [IPMI_PASSWORD] hpm check</p>
Step_8	<p>Put back the selector to bank 1 for future updates</p> <p>RemoteComputer_OSPrompt:~# ipmitool -I lanplus -H &lt;BMC_IP_Addr&gt; -U admin -P admin raw 0x32 0x8f 0x03 0x01</p> <p><i>*See step_4</i></p>
Step_9	<p>Disable IPMI preserve setting (<i>Only if you performed step_5</i>)</p> <p>RemoteComputer_OSPrompt:~# ipmitool -I lanplus -H &lt;BMC_IP_Addr&gt; -U admin -P admin raw 0x32 0x83 0x04 0x01</p> <p><i>*See step_5</i></p>

## Upgrading the BIOS and 10GbE LAN

<b>NOTICE</b>	<ul style="list-style-type: none"> <li>• DO NOT power off or restart the computer device when the system is reading the BIOS or updating the BIOS.</li> <li>• To prevent any errors when updating the FLASH, DO NOT remove the hard disk or USB or any devices in any inappropriate way. An incorrect manipulation will result in a BIOS crash and could prevent the board from booting.</li> <li>• Secure boot must be disabled to perform the upgrades</li> <li>• When scripts end, a full power cycle is performed. This also affects the management controller.</li> </ul>
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## Linux method

### Transferring and uncompressing the package

Step_1	Transfer the latest upgrade package compressed file (zip or tar.gz) to an installed Linux residing on a storage device (M.2, HDD/SSD) of the CG2400.
Step_2	<p>From an OS command prompt, uncompress the zip file. <b>NOTE:</b> To uncompress a zip file, you might need to install an additional Linux package.</p> <pre>tar xzvf &lt;update package tar.gz&gt;</pre> <p>OR</p> <pre>unzip &lt;update package .zip&gt;</pre>
Step_3	<p>Select the proper directory.</p> <pre>cd bios-bundle-&lt;version&gt;</pre>

## Upgrading the BIOS

Step_1	<p>From an OS command prompt, start the upgrade process.</p> <pre>sudo bash ./bios-update.sh</pre>
Step_2	Follow the instructions on screen until the upgrade process is finished. Note that the system will reboot a few times.

## Upgrading the 10GbE LAN

Step_1	<p>From an OS command prompt, start the upgrade process.</p> <pre>sudo bash ./lan-update.sh</pre>
Step_2	Follow the instructions on screen until the upgrade process is finished.

## USB key method

**Note:** This method requires a physical access to the system.

<b>Step_1</b>	Uncompress and copy files to the root of a USB key formatted as FAT32.
<b>Step_2</b>	Insert the USB key in the CG2400, using the front or rear USB port of the CG2400 platform.
<b>Step_3</b>	Reset the system.
<b>Step_4</b>	<p>When the system has restarted, press F6 to activate the boot menu and select USB key.</p> <p><b>NOTE:</b> You can also press F2 or DEL, enter the BIOS menu, go to the <b>Save &amp; Exit</b> tab and select the USB key under Boot Override.</p> <p>Do not press &lt;ESC&gt;. This will bring you in the EFI shell, thus requiring to reboot the CG2400 again to boot from the USB key.</p>
<b>Step_5</b>	<p>A menu will appear.</p> <p>Select what you want to do:</p> <ul style="list-style-type: none"> <li>• Exit (press 1)</li> <li>• Update BIOS (press 2)</li> <li>• Update 10Gb LAN (press 3)</li> </ul> <p><b>NOTE:</b> The system will perform a full power cycle after updating the BIOS or the 10Gb LAN.</p>

## 5/Individual Changelogs

### 5.1. BIOS

#### 5.1.1. Changes Since SUP15

- No Changes

#### 5.1.2. Changes Since SUP14

- Improvement
  - Updated to code drop 5.14\_PurleyCrb\_0ACLA063

#### 5.1.3. Changes Since SUP13

- Bug Fixes
  - Fixed an issue where the time set in NTP is incorrect after a reset (CG2400M-2086)

#### 5.1.4. Changes Since SUP12

- Bug Fixes
  - Fixed issue where BIOS passwords were not case-sensitive (CG2400M-2077)

#### 5.1.5. Changes Since SUP11

- Bug Fixes
  - Fixed issue where the Power Button would not work and freeze the shutdown commands (CG2400M-2062)
- Improvements
  - Update Microcode, ME firmware and security fixes to Intel® SPS\_04.01.04.804

#### 5.1.6. Changes since SUP10

- Improvements
  - Added KCI "HDD Security Protocol Priority" in the setup menu for Self-Encrypting Drives feature. (CG2400M-2059)

#### 5.1.7. Changes since SUP09

- Bug Fixes
  - Fixed an issue where two sensor IDs were identical (CG2400M-2057)
  - Fixed an issue where the Watchdog was not restarted after a system reset (CG2400M-1870)

#### 5.1.8. Changes since SUP08

- Bug Fixes
  - Fixed an issue where the temperature sensors were not displaying their value
  - Fixed an issue where an unnecessary IPMI event was seen before and after sending the "set sel time" command.



### 5.1.9. Changes since SUP07

- Bug Fixes
  - Fixed issue where BIOS settings were not fully editable with AMI SCE tool
  - Fixed issue where the "Ipv4 PXE Support" option is not exposed with the SCT Linux tool
  - Fixed issue where the "Setup Prompt Timeout" option cannot be updated with the SCT Linux tool
- Improvements
  - Update Microcode, ME firmware and security fixes to Intel® SPS\_04.01.04.505

### 5.1.10. Changes since SUP06

- No BIOS changes

### 5.1.11. Changes since SUP05

- Improvements
  - Updated Microcode, ME firmware and Security fixes by Intel

### 5.1.12. Changes since SUP04

- Bug Fixes
  - Fixed an issue where boot order was lost when rebooting server
  - Fixed issue where sending the command chassis identify would leave the LED hard lit and not blinking.
  - Fixed issue when Last Power State is selected but was not applied.
- Improvements
  - Added the Power On Delay feature
  - Updated the Microcode
  - Corrected the SMBUS Type 8 connector Fan order description

### 5.1.13. Changes since SUP03 (SUP03 was not released in production)

- Improvements
  - Added support for Minimum Fan Speed override feature. (CG2400M-1976)
  - Improved the BIOS update process via Linux (CG2400M-1981)

### 5.1.14. Changes since SUP02

- Bug Fixes
  - Fixed issue where the Fan order was incorrect (i.e. Fan 1 was 6, 2 was 5, etc.)

### 5.1.15. Changes since SUP01

- Bug Fixes
  - Fixed an issue where some undesired characters were displayed in the serial console following a system reboot. (CG2400M-1902)
  - Fixed issue where the OnBoard devices were no longer shown in the "Option ROM Dispatch Policy" setup menu after reboot. (CG2400M-1937)
  - Fixed issue where the Region B 10G LAN was not present after a flash update. (CG2400M-1876)
  - Fixed issue where PXE Boot from LAN was not present in Secure Boot Mode. (CG2400M-1905)
- Improvements
  - Updated KMB-IXS100 to latest AMI core module. (CG2400M-1947)
  - Set longer timeout to avoid BMC sync command errors at POST. (CG2400M-1893)

- Added necessary files to update BIOS and LAN via linux and efi shell. (CG2400M-1876)
- Added POST messages during BIOS boot about Redfish state. (CG2400M-1893)

## 5.1.16. Changes since BETA version

- **Bug fixes**
  - Fixed issue where an undocumented PEF actions was present in the SEL when original BIOS events for memory errors used sensor #08. (CG2400M-1784)
  - Fixed an issue where the BIOS service to detect and log errors to the BMC SEL was not using the proper I/Os used by the BMC. (CG2400M-1383 & CG2400M-1618)
  - Fixed Socket menu which should not have contained entries for Socket 2 & 3. (CG2400M-1360)
  - Fixed bad pointer arithmetic in SMBIOS table. (CG2400M-1360)
  - Fixed AFU ME update hang and SMBIOS type 0 to report proper version number. (CG2400M-1517 & 1590)
  - Fixed issues related to option ROM menus and updated AMITSE. (CG2400M-768, 1348, 1533 & 1535)
  - Fixed issue where continuous reboot would occur when secure boot is enabled before firmware is reflashed. (CG2400M-1812)
  - Fixed issue where the watchdog timer would not stop when the HDD password prompt was shown, forcing a system reboot systematically after a few seconds. (CG2400M-1380)
- **Improvements**
  - Updated KMB-IXS100 to latest AMI core module. CSM is now disabled by default. (CG2400M-1880)
  - Removed BIOS recovery feature because it is replaced by BMC BIOS snapshot feature. (CG2400M-1563 & 1205)
  - Update to SMBIOS table Type 0, 1, 2, 3, 8, 9, 16 & 17. (CG2400M-1404, 1405, 1420, 1429, 1555, 1556 & 1608)
  - Enabled and modified BMC Power Control AMI module. (CG2400M-1819)
  - Added support for DIMM margining sensors. (CG2400M-1806)
  - Corrected the Kontron Logo in the BIOS menu and removed 2 extraneous dots during POST. (CG2400M-1526)
  - Adjusted various GPP Pins and disabled sensor settings unused with KMB-IXS100. (CG2400-1813)
  - Enabled secure boot key generation feature. (CG2400M-1380)
  - Adapted ACPI USB configuration. (CG2400M-623)
  - Added setup option "Hdd Security Freeze Lock : enable", disabling this options allows secure erase. (CG2400M-1379)
  - Added SDL tokens to allow board specific SEL sensor numbers for RAS error logging by the BIOS. (CG2400M-1458)
  - Added Redfish 1.7 modules and dependencies, and disabled unused modules. (CG2400M-1802)
  - Adapted AptioV module for PptionRomPolicy for proper number of on-board and slot devices. (CG2400M-780)
  - Removed BIOS options not taken into account when another options is used to achieve the same result. (CG2400M-1414)
  - Removed the Power State BIOS menu as it is invalidated by IPMI chassis policy command. (CG2400M-1780)

## 5.2. BMC

### 5.2.1. Changes Since SUP15

- **Improvement**
  - Added the Sensor Name field to the downloadable event log on the WebUI (CG2400M-2125)
- **Bug Fixes**
  - Fixed an issue where the Redfish sensor get was not reflecting the actual sensor states (CG2400M-2147)

- Fixed an issue where the Value format on the FAN Speed sensor was incorrect (CG2400M-2151).

### 5.2.2. Changes Since SUP14

- Improvements
  - Fixing possible vulnerabilities (CVE-2023-48795 and CVE-1999-0524) (CG2400M-2135)
- Bug Fixes
  - Fixed an issue where an alarm for lower critical for fans would not clear once issue was resolved (CG2400M-2134)
  - Fixed issue where the system boot time was slower due to numerous SEL events (SUS-947)
  - Fixed issue where an alarm would be generated due to start up fans speed (CG2400M-2136)
  - Fixed issue where the system would stop logging new SEL events (CG2400M-2115).

### 5.2.3. Changes Since SUP13

- Bug Fixes
  - Fixed an issue where the time set in NTP is incorrect after a reset (CG2400M-2086)
  - Fixed an issue where the board status reports undocumented reset events (CG2400M-2087)
  - Fixed an issue where a sensor was reporting a wrong value when disabled (CG2400M-2109)
  - Fixed issue where when an address is added in NTP it is invalid but no message appears (CG2400M-2082)
  - Fixed an issue where the Dashboard is not displaying events properly (CG2400M-2084)
- Improvements
  - Added a feature to Enable/Disable root access on BMC while using a jumper (CG2400M-2119)
  - Fixed a vulnerability where it was possible to send a script to the BMC via the Redfish Host Interface (CG2400M-2116)
  - Added a PWROK Capture Sensor (CG2400M-2081)

### 5.2.4. Changes Since SUP12

- Bug Fixes
  - Fixed issue where a false alarm was seen in a 1 CPU environment (CG2400M-2075)
- Improvements
  - Removed 2 sensors, CATERR and CPU\_ERR2, that were duplicates (CG2400M-2076)
  - Fixed the "V\_3V\_BAT" sensor thresholds to be more accurate (CG2400M-2078)

### 5.2.5. Changes since SUP11

- Bug Fixes
  - Fixed issue where Non-Critical threshold for DIMM Temp (MRGN) would generate a "false" alarm (CG2400M-2072)
  - Fixed issue where UUID was changed/recreated when BMC was upgraded (CG2400M-2069)
  - Fixed issue where an event was missing from the EVENT LOG after Reset Server (CG2400M-2064)

### 5.2.6. Changes since SUP09

- Bug Fixes
  - Fixed issue where fan would have inconsistent speed at system startup (CG2400M-2058)
- Improvements
  - Disabled the use of TLSv1.0 & TLSv1.1 to enhance security (CG2400M-2061)

### 5.2.7. Changes since SUP08

- Bug Fixes

- Fixed issue where the help button on the KVM Record Setting page was not functional (CG2400M-2054)
  - Fixed issue where the today chart on the Web UI was not displaying proper information (CG2400M-2051)
- Improvements
  - Added the possibility to control the "Chassis Identify" LED from the WebUI (CG2400M-2052)

## 5.2.8. No changes in SUP09

## 5.2.9. Changes since SUP07

- Bug Fixes
  - Fixed issue when there is only 1 CPU, would result in the Sensor 'P2 TJMAX' to report wrong status
  - Fixed issue where the sensor 'Ver Change BMC' did not trigger an event when the minor version is greater than 10
  - Fixed issue where IPMI Set SEL time command would generate unnecessary PEF events
  - Fixed issue where BMC ignores set SEL time sent from BIOS to sync time
- Improvements
  - Implement basic support for TAM LEDs

## 5.2.10. Changes since SUP06

- Improvements
  - Added New Sensors to help debug system (CG2400M-2023/2024/2025)
  - Removed P1/P2 TJMAX UC & UNC thresholds since not-applicable (CG2400M-2028)
  - Removed Set Sel Time / Date from WebUI since it was not functional
- Bug Fixes
  - Fixed issue where power supply sensors are unavailable when powered down (CG2400M-2029)
  - Fixed issue where false P1/2 DTS Thrm Mrgn events were generated in the SEL Logs (CG2400M-2010/2035)
  - Fixed issue where SEL TIME GET wasn't reporting proper time after power cycle

## 5.2.11. Changes since SUP05

- Bug Fixes
  - Fixed issue where a False Fan Failure SEL event was produced when there was a system reboot (CG2400M-1986)
  - Fixed Issue where the system would not reboot when there was a loss of power during the Power On Delay countdown (CG2400M-2015)
  - Fixed the system LED behavior to match the behavior stated in the User Guide (CG2400M-2014)
  - Fixed issue where sending the command <chassis identify force> would not work (CG2400M-2012)
  - Fixed issue where the system would become unresponsive following an HPM upgrade (CG2400M-2011)
  - Fixed issue where sending the command <HPM activate> when not needed would cause the system to freeze. (CG2400M-2019)
- Improvements

## 5.2.12. Changes since SUP04

- Bug Fixes
  - Fixed an issue where sensors wrongly generated SEL events when the system was power cycled
  - Fixed issue where clearing the SEL in the WebUI would cause the system to hang.

- Improvements
  - Updated Event/Reading Type Code for Fan failure to match documentation
  - Added the Power On Delay feature
  - Updated WebUI to clarify requirements when changing user password.

### 5.2.13. Changes since SUP03 (SUP03 was not released in production)

- Bug Fixes
  - Fixed issue where BMC is unreachable following a chassis power cycle when a static IP address is configured. (CG2400M-2000)
- Modifications
  - Added OEM command to preserve Threshold values after cold reset of the BMC (CG2400M-1992)
  - Added support for Minimum Fan Speed override feature. (CG2400M-1976)

### 5.2.14. Changes since SUP02

- Bug Fixes
  - Fixed issue where the FAN OID was incorrect during a SNMP trap event
  - Fixed issue where the Fan order was incorrect (i.e. Fan 1 was 6, 2 was 5, etc.)
  - Fixed issue where fan speed remained low during the upgrade process causing the system to heat.
  - Fixed issue where sending "Persistent" ipmi command for Bootdev was not functional via KCS interface.

### 5.2.15. Changes since SUP01

- Bug Fixes
  - Fixed issue where error message were reported after ipmitool fru edit commands. (CG2400M-318)
  - Fixed issue where IPMI commands were disabled after BMC was completely booted. (CG2400M-1903)
  - Fixed issue where false events were reported from DTS Thrm Mrgn sensors during BIOS boot. (CG2400M-1953)
  - Fixed issue where no value was reported from the DTS Therm Mrgn sensor when using a single CPU configuration. (CG2400M-1899)
  - Fixed issue where a timeout error was generated when using UEFI Recovery command. (CG2400M-1964)
  - Fixed issue where the wrong value was returned when using UEFI Recovery command. (CG2400M-1961)
  - Fixed issue where DIMM sensor crossed UNC (Upper-Non Critical) threshold when system was at idle. (CG2400M-1950)
  - Fixed issue where the fan speed could not be read after power off. (CG2400M-1904)

### 5.2.16. Change since BETA version

- Bug Fixes
  - Fixed issue with fan speed thresholds that weren't properly taken into consideration. (CG2400M-1787)
  - Fixed issue where false Fan fault were reported after first boot. (CG2400M-1527)
  - Fixed issue where multiple platform events were received from PSU sensors following a system reset initiated by IPMI commands. (CG2400M-1866)
  - Fixed issue where PSU presence sensor would trigger false alerts while the system is idle. (CG2400M-1577)
  - Fixed issue where PSU2 Input Power sensor presence would regularly disappear. (CG2400M-1855)
  - Fixed issue where P1/P2 DTS Thrm Mrgn sensor would report undesired events on boot. (CG2400M-1865)
  - Fixed issue where presence of CPU#\_DTS\_TEMP\_MRGN sensors were not properly detected (CG2400M-1875)
  - Fixed issue where missing CPU events were sent on a single CPU configuration. (CG2400M-1528)

- Fixed issue where DIMM temp sensors would report "Disabled" instead of "No Reading". (CG2400M-1837)
- Fixed issue where SNMP Sub-agent would report unknown cooling fan status. (CG2400M-1354)
- Fixed issue where false "FRU Corruption" was observed on PSU FRUs. (CG2400M-1854)
- Fixed issue where SEL was not preserved during HPM upgrade. (CG2400M-1829)
- Fixed issue where MCE and IERR were not properly logged in the SEL. (CG2400M-1773)
- Fixed issue where jumper J36 would not disable the Watchdog timer as intended. (CG2400M-1431)
- Fixed issue where values in Redfish using Firefox were not refreshed properly. (CG2400M-1778)
- Fixed issue where the BMC would not boot properly after pressing power button. (CG2400M-1828)
- Fixed issue where after ~20 BMC power cycles, the BMC would crash, causing multiple issues, including HPM upgrade failures. (CG2400M-1821)
- Fixed issue where Watchdog would bite during WebUI BMC upgrade, resulting sometimes into corrupted BMC. (CG2400M-1803)
- Fixed issue where sensors connected to the Node Manager were not deasserting events properly. (CG2400M-1330)
- Fixed issue where the Power button would not work when power policy is set to "always-off" (CG2400M-1779)
- Fixed issue where HPM upgrade would sometimes be impossible following an AMI MegaRAC core upgrade. (CG2400M-1628)
- Fixed issue where the BIOS menu was slowed down while the BMC was booting. (CG2400M-1610)
- Fixed issue where PCIe Temp would show as 0 when the probe is disconnected. It's now showing no reading. (CG2400M-1367)
- Fixed issue where PCIe sensor was not detected after bootup. (CG2400M-1824)
- **Improvements**
  - Configured Battery and M.2 temperature sensors default thresholds to more realistic values. (CG2400M-1890)
  - System Event Log can now be cleared remotely using Redfish API. (CG2400M-1749)
  - Implementation of Redfish production provisioning. (CG2400M-1790)
  - Removed unused Redfish modules and associated buttons in the BMC WebUI. (CG2400M-1802)
  - Added support for BIOS snapshot creation and restore using IPMI OEM commands. (CG2400M-1840)
  - Updated various FRU information fields default values. (CG2400M-1651 & 1874)
  - Added PDB FRU info. (CG2400M-1188)
  - Configured default thresholds for P1 and P2 TJMAX sensors to avoid issues with some IPMI clients. (CG2400M-1849)
  - Updated P2 DIMM temperature sensor thresholds. (CG2400M-1578)
  - Updated DIMM Thermal margin sensors behavior for more accurate reporting.
  - Increased the temperature sensors reading priority to improve the fan responsiveness. (CG2400M-1580)
  - I210 1Gbe port is now disabled by default on the payload. (CG2400M-1833)
  - Removed unused Jviewer KVM button from the WebUI. (CG2400M-1788)
  - Changed some reading format of a few sensors in the WebUI. (CG2400M-1576)
  - Fixed some typos observed in the WebUI. (CG2400M-1651)
  - Removed SNMP v2c support on the BMC. (CG2400M-1335)
  - Removed the possibility to communicate with the BMC through the built-in 10 Gbe interfaces. (CG2400M-1786)
  - Updated MegaRAC SP-X core. (CG2400M-1519)

## 5.3. FPGA

### 5.3.1. Change since BETA version

- **Improvements**
  - Only the version changed to avoid having a 0.x FW version in a GA release.

## 5.4. LAN EEPROM

- No Changes since BETA Version

## 5.5. KSNMPD

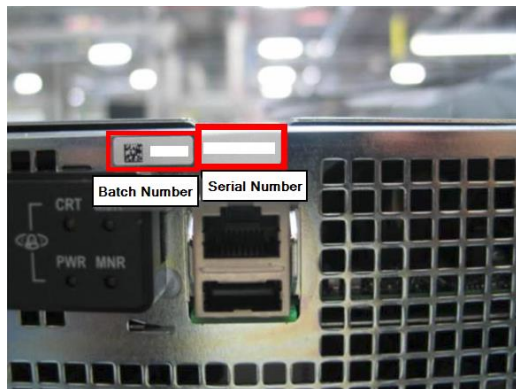
### 5.5.1. Changes since 1.2.3

- Bug Fixes
  - Fixed issue where a sensor with a negative value would cause a SEL event. (CG2400M-2028)
  - Fixed issue where removal of a PSU would result in the wrong status. (CG2400M-2033)

## 6/How to identify your product

New production units of the CG2400 will not have the applied modification described herein. CG2400 with the batch ID number greater than 0100000008 will be setup with SUP09 unless specified by customer's contract. The batch ID number is indicated on a label next to the serial number label as illustrated below.

- Serial numbers can be verified using the BMC Web UI page or with IPMITOOL.
- Product's batch revision number can be identified through its product label, shown below.



Labels are located in this area.  
Exact placement varies according to the manufacturing date.

Scheme:

P/N:	CGxxxxxxxx
S/N label:	90xxxxxxxx
BATCH Scheme:	00xxxxxxxx

#### About Kontron

Kontron, a global leader in embedded computing technology and trusted advisor in IoT, works closely with its customers, allowing them to focus on their core competencies by offering a complete and integrated portfolio of hardware, software and services designed to help them make the most of their applications.

With a significant percentage of employees in research and development, Kontron creates many of the standards that drive the world's embedded computing platforms; bringing to life numerous technologies and applications that touch millions of lives. The result is an accelerated time-to-market, reduced total-cost-of-ownership, product longevity and the best possible overall application with leading edge, highest reliability embedded technology.

Kontron is a listed company. Its shares are traded in the Prime Standard segment of the Frankfurt Stock Exchange and on other exchanges under the symbol "KBC".

For more information, please visit: <http://www.kontron.com/>



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